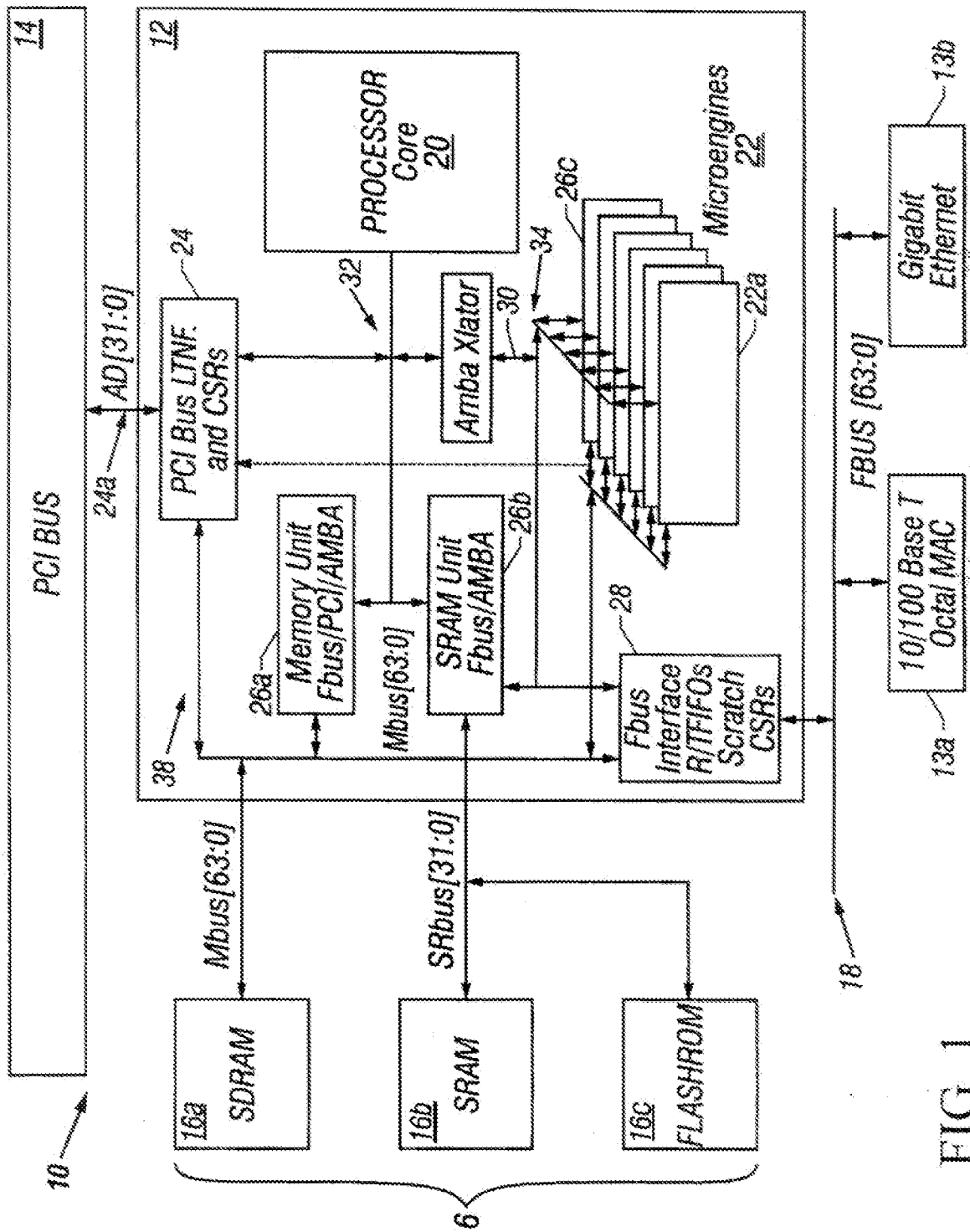
14
PCI BUS
10
12
AD[31:0]
24a
24
PCI Bus LTNF. and CSRs
PROCESSOR Core 20
32
Arma Xlator
30
34
26c
Microengines 22
22a
Memory Unit Fbus/PCI/AMBA
26a
SRAM Unit Fbus/AMBA
26b
Mbus[63:0]
28
Fbus Interface R/TFIFOs Scratch CSRs
38
FBUS [63:0]
Gigabit Ethernet
13b
10/100 Base T Octal MAC
13a
18
Mbus[63:0]
SRbus[31:0]
16a SDRAM
16b SRAM
16c FLASHROM
16

FIG. 1